

ABSTRACT OF THE DISCLOSURE

A liquid crystal display having a substrate includes a plurality of gate lines extended from each gate on the substrate, a gate insulating layer on the substrate including the gate lines, a plurality of data lines arranged to be perpendicular to the gate lines, a passivation layer over the data lines and the gate insulation layer, a plurality of etching holes in the passivation layer and the gate insulating layer, wherein the gate insulating layer within the etching holes has at least one concave and convex portions, and a plurality of seal pattern lines in the etching holes.